



MC3373

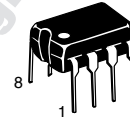
Remote Control Amplifier/Detector

The MC3373 is intended for application in infrared remote controls. It provides the high gain and pulse shaping needed to couple the signal from an IR receiver diode to the tuning control system logic.

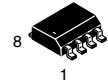
- High Gain Pre-Amp
- Envelope Detector for PCM Demodulation
- Simple Interface to Microcomputer Remote Control Decoder
- Use with Tuned Circuit for Narrow Bandwidth, Lower Noise Operation
- Minimum External Components
- Wide Operating Supply Voltage Range
- Low Current Drain
- Improved Retrofit for NEC Part No. μ PC1373
- MC14497 Recommended IR Transmitter
- MLED81 Complementary Emitter
- MRD821 Complementary Detector Diode

REMOTE CONTROL WIDEBAND AMPLIFIER WITH DETECTOR

SEMICONDUCTOR TECHNICAL DATA



P SUFFIX
PLASTIC PACKAGE
CASE 626

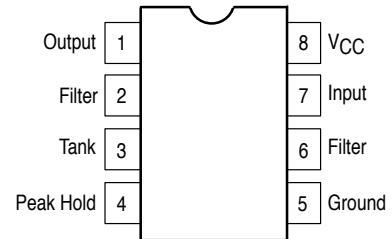


D SUFFIX
PLASTIC PACKAGE
CASE 751
(SO-8)

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Supply Voltage	V_{CC}	15	Vdc
Operating Temperature Range	T_A	0 to 75	$^{\circ}C$
Storage Temperature Range	T_{stg}	-55 to +125	$^{\circ}C$
Junction Temperature	T_J	150	$^{\circ}C$
Power Dissipation, Package Rating Derate above 25 $^{\circ}C$	P_D $1/\theta_{JA}$	1.25 10	W mW/ $^{\circ}C$

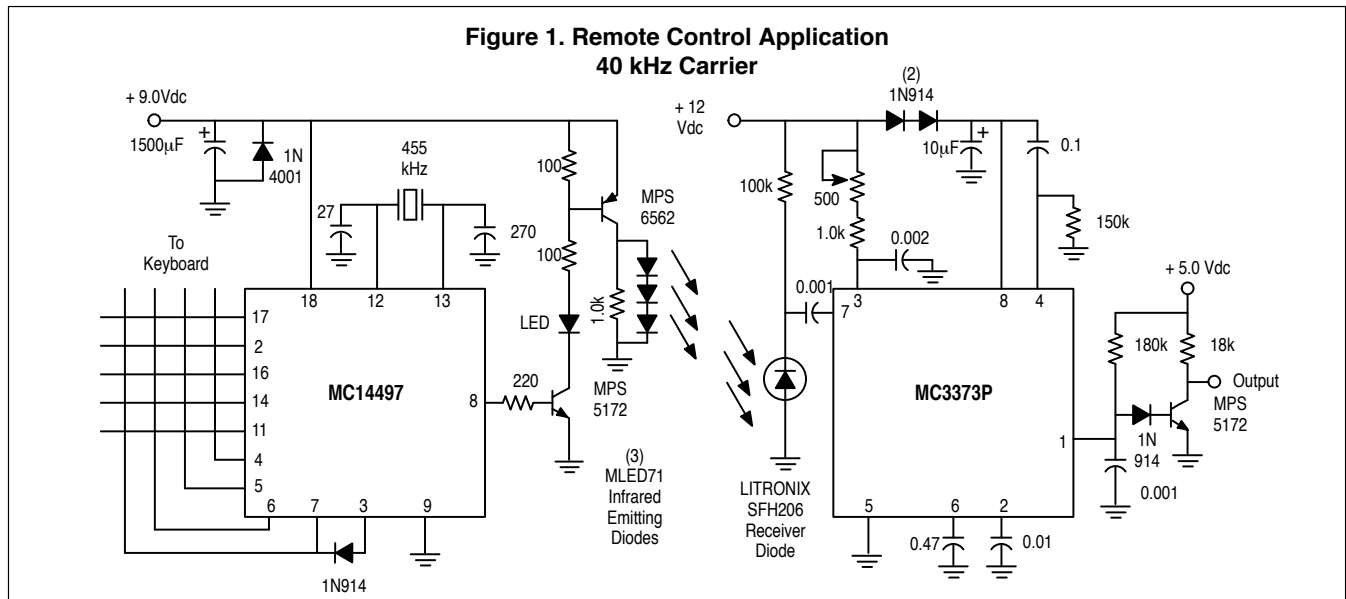
PIN CONNECTIONS



ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC3373P	$T_A = 0$ to $+75^{\circ}C$	Plastic DIP
MC3373D		SO-8

Figure 1. Remote Control Application
40 kHz Carrier



RECOMMENDED OPERATING CONDITIONS

Characteristics	Symbol	Min	Typ	Max	Unit
Power Supply Voltage (25°C)	V _{CC}	4.75	–	15	Vdc
Power Supply Voltage (0°C)	V _{CC}	5.0	–	15	Vdc
Input Frequency	f _{in}	30	40	80	kHz

ELECTRICAL CHARACTERISTICS (T_A = 25°C, V_{CC} = 5.0 V, f_{in} = 40 kHz, Test circuit of Figure 2)

Characteristics	Symbol	Min	Typ	Max	Unit
Power Supply Current	I _{CC}	1.5	2.5	3.5	mAdc
Input Terminal Voltage	V(Pin 7)	2.4	2.8	3.0	Vdc
Input Voltage Threshold	V _{in}	–	50	100	μVpp
Input Amplifier Voltage Gain (V[Pin 3] = 500 mVpp)	A _v	–	60	–	dB
Input Impedance	r _{in}	40	60	80	kΩ
Output Voltage, V _{in} = 1.0 mVpp	V _{OL}	–	–	0.5	V
Output Leakage, V _{CC} = V _{OH} = 15 Vdc	I _{OH}	–	–	2.0	μA
Output Voltage, Input Open	V _{OH}	–	–	5.0	Vdc

Figure 2. Test Circuit

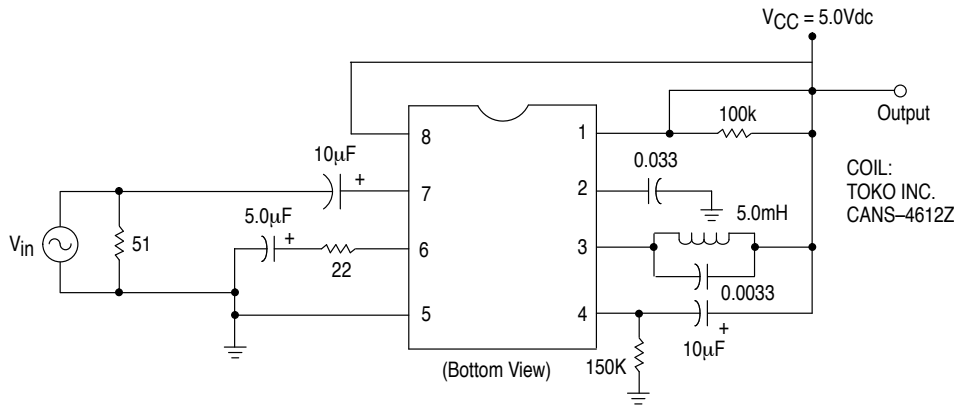


Figure 3. Representative Block Diagram

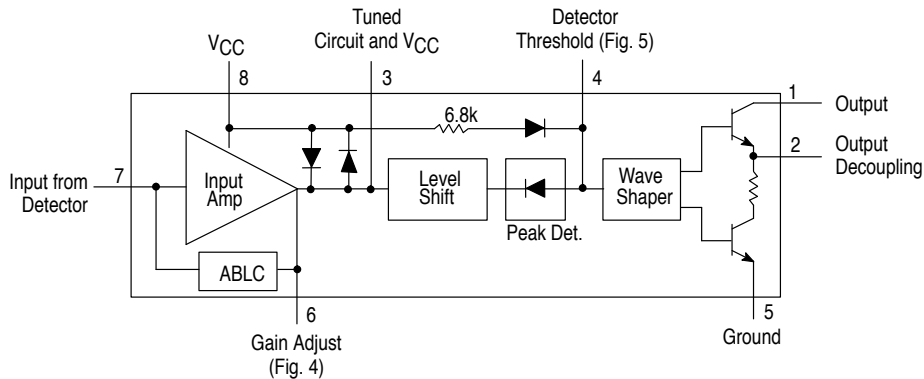


Figure 4. Input Amplifier Gain

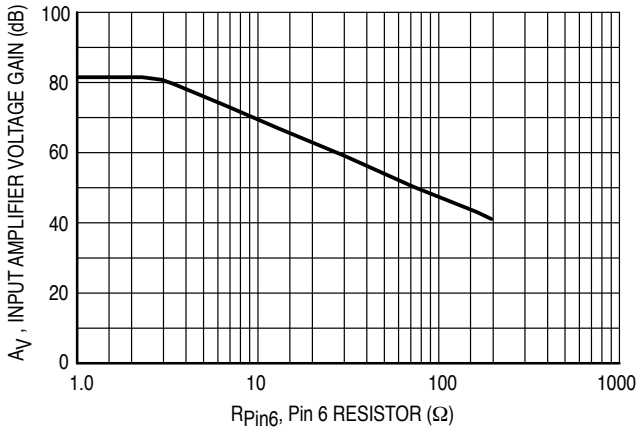
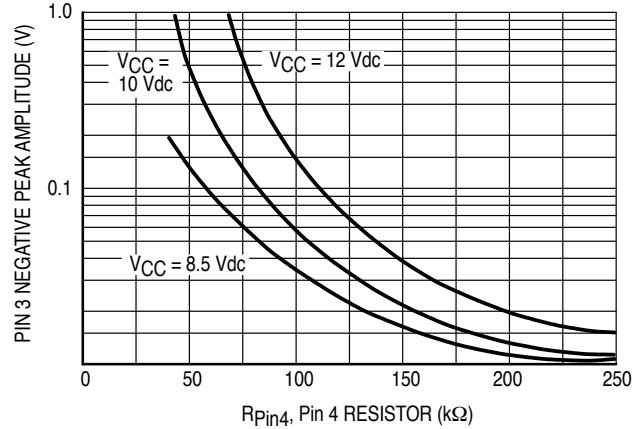


Figure 5. Detector Threshold

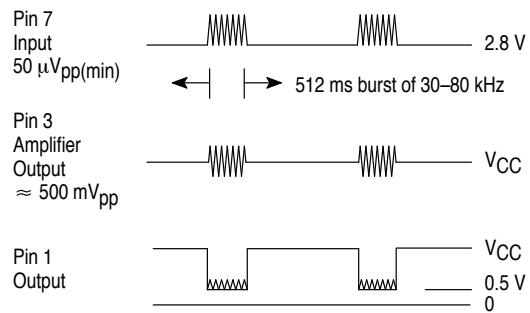


APPLICATIONS INFORMATION

The MC3373 is a specialized high gain amplifier/signal processor bipolar analog IC designed to be the core of infrared carrier signaling systems. The amplifier section has an Automatic Bias Level Control (ABLC) for simplified direct connection to an IR detector diode. Generally, it is operated ac coupled, utilizing an input high-pass filter to eliminate power line related noise, particularly that from florescent and gas vapor lamps. The use of a high frequency carrier is strongly recommended as opposed to simply detecting “dc” bursts of IR energy. In the carrier mode setup the MC3373 acts like an AM receiver subsystem, amplifying the incoming signal, demodulating it, and providing some basic wave shaping of the demodulated envelope. The tuned circuit at Pin 3 provides the main system selectivity reducing random noise interference and permitting multichannel operation in the same physical area without falsing. In the multichannel case the carriers must not be harmonically related. The bandwidth is determined primarily by the “Q” of the coil. Bandwidth may be increased by loading, shunting, the coil with a resistor.

Since this is a very high gain system operating at relatively high frequencies, care **must** be taken in the circuit layout and construction. Do not use wire wrap or non-ground plane protoboard. A simple single sided PCB with ground fill or a two-sided board with a solid groundplane and top side point-to-point will provide consistent high performance. There is a wide array of IR emitter/detectors available. The Motorola MLED81 and MRD821 are an excellent low cost combination to use with the MC3373. Multiple emitters are recommended for extended range.

Figure 6. Typical Signal Waveforms



The input amplifier gain is approximately equal to the load impedance at Pin 3, divided by the resistor from Pin 6 to ground. Again, the low frequency gain can be reduced by using a small coupling capacitor in series with the Pin 6 resistor.

The load may be resistive, with only, or tuned, as in the test circuit. The amplifier output is limited by back-to-back clamping diodes, level shifted, buffered and fed to a negative peak detector. The detector threshold is set by the external resistor on Pin 4, and an internal 6.8 kΩ resistor and diode to V_{CC}. The capacitor from V_{CC} to Pin 4 quickly charges during the negative peaks and then settles toward the set-up voltage between signal bursts at a rate roughly determined by the value of the capacitor and the 6.8 k resistor. The external capacitor at Pin 2 filters the ultrasonic carrier from the pulses.

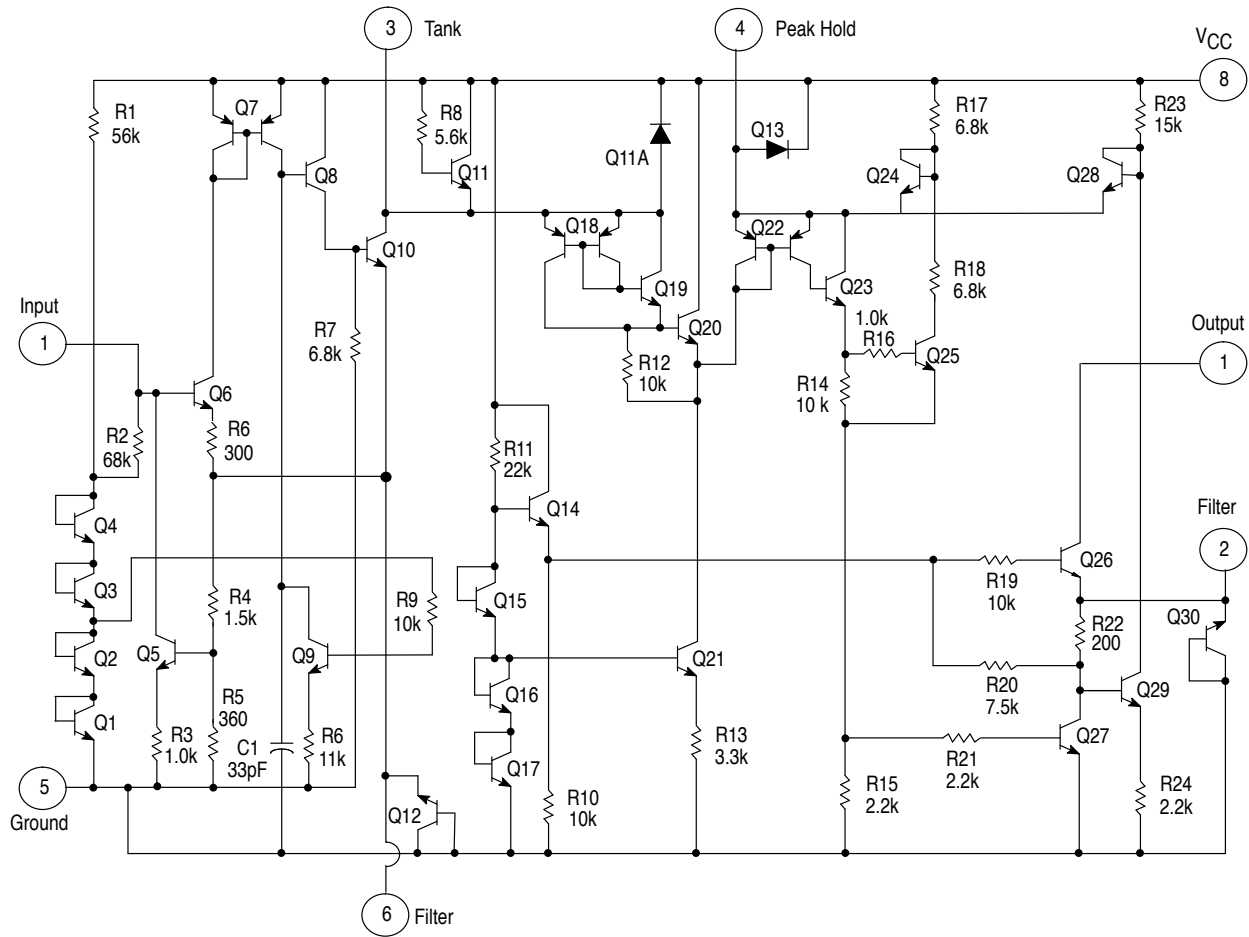
MC3373

CIRCUIT DESCRIPTION

Q1 to Q4 set the bias on the amplifier input at approximately 2.8 V. Q6 to Q10 form the input amplifier, which has a gain of about 80 dB when R(Pin 6) = 0, Q5 sinks input current from the photo diode and keeps the amplifier properly

biased. Q18 to Q20 level shift and buffer the signal to the negative peak detector, Q22 and Q23. Output devices Q26 and Q27 conduct during peaks and pull the output (Pin 1) low. The capacitor on Pin 2 filters out the carrier.

Figure 7. Representative Schematic Diagram



OUTLINE DIMENSIONS

P SUFFIX
PLASTIC PACKAGE
CASE 626-05
ISSUE K

NOTE 2

SEATING PLANE

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.40	10.16	0.370	0.400
B	6.10	6.60	0.240	0.260
C	3.94	4.45	0.155	0.175
D	0.38	0.51	0.015	0.020
F	1.02	1.78	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	0.76	1.27	0.030	0.050
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	—		10°	
N	0.76	1.01	0.030	0.040

$\oplus \text{ } \varnothing 0.13 (0.005) \text{ } \textcircled{M} \text{ } T \text{ } A \text{ } \textcircled{M} \text{ } B \text{ } \textcircled{M}$

- NOTES:
1. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
 2. PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS).
 3. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.


D SUFFIX
PLASTIC PACKAGE
CASE 751-05
(SO-8)
ISSUE N

SEATING PLANE

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.80	5.00	0.189	0.196
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 BSC		0.050 BSC	
J	0.18	0.25	0.007	0.009
K	0.10	0.25	0.004	0.009
M	0°		7°	
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

$\oplus \text{ } 0.25 (0.010) \text{ } \textcircled{M} \text{ } T \text{ } B \text{ } \textcircled{S} \text{ } A \text{ } \textcircled{S}$

- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.
 3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

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